

50454-B600

PATENT

ART UNIT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Docket No.

30/DBP/B600

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Commissioner:

Broadcom Corporation, assignee of the entire interest in and to each U.S. patent application, identified on Exhibit A attached, under an Assignment recorded in the U.S. Patent and Trademark Office, hereby revokes all previous Powers of Attorney and appoints:

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all members or associates of or of counsel to the firm CHRISTIE, PARKER & HALE, LLP, telephone (626) 795-9900, as principal attorneys with power to appoint associate attorneys, to prosecute this application and any subsequent application based on the disclosure of this application, and to transact all business in the Patent and Trademark Office connected with this application and any subsequent application.

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The authority under this Power of Attorney of each person named above shall automatically terminate and be revoked upon such person ceasing to be a member or associate of or of counsel to that law firm.

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Date: 7/23/3

Print Name: Dee Henderson

Title: Intellectual Property Portfolio

Manager

DBP/djp

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CPH/BP NO.

APPECATION NO. FILING DATE

TITLE

50449/BP1406	09/611,809	July 7, 2000	Encryption Processor
			for Performing
			Accelerated
			Computation to
			Establish Secure
		į	network Sessions
			Connections
50454/BP1421	09/827,882	April 4, 2001	Authentication engine
			Architecture and
			Method
50464/BP1567.3	09/859,330	May 16, 2001	Methods and
			Apparatus for
			Hardware
		1	Normalization and
			Denormalization
50468/BP1688.1	09/892,240	June 26, 2001	Methods and
			Apparatus for
			Implementing a
			Cryptography Engine
50472/BP1688.3	09/892,242	June 26, 2001	Methods and
			Apparatus for
	;		Implementing a
	, 		Cryptography Engine
50470/BP1688.2	09/892,310	June 26, 2001	Methods and
			Apparatus for
		· [Implementing a
		:	Cryptography Engine
50462/BP1567.2	09/916,557	July 26, 2001	Stream Cipher
	· ·		Encryption
			Application
			Accelerator and
			Methods Thereof
50460/BP1567.1	09/929,178	August 14, 2001	E-commerce Security
			Processor Alignment
			Logic
50466/BP1567.4	09/948,203	September 6, 2001	Methods and
			Apparatus for
			Implementing a
			Cryptography Engine
50479/BP2054	10/004,458	October 23, 2001	Methods and
			Apparatus for
· .			Collapsing Interrupts

50476/BP2009	10/026,109	December 20, 2001	Methods and
			Apparatus for
		1	Accelerating ARC4
		· ·	Processing
50456/BP1687	10/042,019	January 8, 2002	Fast SHA1
			Implementation
50487/BP2280	10/160,143	May 31, 2002	Methods and
		i	Apparatus for
		ļ	Accelerating Secure
			Session Processing
50488/BP2281	10/160,322	May 31, 2002	Methods and
			Apparatus for
			performing
]			Encryption and
			Authentication
50484/BP2278	10/160,335	May 31, 2002	Methods and
		İ	Apparatus for
			Performing
			Authentication and
			Decryption
50486/BP2279	10/161,475	May 31, 2002	Data Transfer
			Efficiency in a
			Cryptography
			Accelerator System
50443/BP1405CON	10/227,491	August 23, 2002	Security Chip
			Architecture and
		· [Implementations for
	4		Cryptography
			Acceleration
50482/BP2093	10/330,694	December 24, 2002	Methods and
			Apparatus for
			Performing Hash
			Operations in a
		•	Cryptography
			Accelerator
50501/BP2410	10/350,902	January 23, 2003	Cryptography
	· ·		Accelerator Interface
			Decoupling from
			Cryptography
			Processing Cores
50499/BP2409	10/350,922	January 23, 2003	Cryptography
			Accelerator Data
			Routing Unit

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TITLE

50495/BP2404	10/351,258	January 23, 2003	Methods and Apparatus for Ordering Data in a Cryptography Accelerator
50490/BP2406	10/378,035	February 27, 2003	Methods and Apparatus for Initialization Vector Processing

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